



IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

Response under 37 CFR 1.116
Expedited Procedure
Examining Group 2138

Patent Application

Inventor(s) Josef Schmid

Case 3

Serial No. 10/075,868 Group Art 2138

File Date February 13, 2002

Examiner John J. Tabone, Jr.

Title Boundary Scan Delay Chain For Cross-Chip
Delay Measurement

ASSISTANT COMMISSIONER FOR PATENTS AND TRADEMARK
WASHINGTON, DC 20231

Dear Sir:

AMENDMENT AFTER FINAL

In response to the Office Action dated April 27, 2006, please consider the following remarks on the above-identified patent application as follows.

*Do Not Enter
JJT 8/8/06*

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop AF, Director of the US Patent and Trademark Office, PO Box 1450, Alexandria, VA 22313-1450, on

7/18/ 2006.

Margaret Cardoso
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